

Employee Job Description



Job Title	Layout Design Engineer
Department	Product Development
Location	San Diego, CA
Job Level	Staff ~ Principal
Reports to (title)	Manager

JOB PURPOSE:

Luxtera is seeking a layout design engineer to produce high-quality physical design for our flagship silicon photonics transceiver product.

ESSENTIAL FUNCTIONS OF THIS POSITION:

Layout and physical verification of high-speed (up to 50Gb/s), high-precision analog/mixed signal circuits in FinFET process—in addition to design of state-of-the-art silicon photonics integrated circuit layout

QUALIFICATIONS/SKILLS:

- Proficient in using layout editing tools in the Cadence Virtuoso design environment
- Solid working knowledge of debugging DRC/LVS/ERC with Cadence PVS or Mentor Calibre
- Conceptual understanding of layout topics such as parasitics, matching, crosstalk, transistor layout dependent effects, latchup, IR drop, electromigration (EM), and deep nwell
- Experience in chip-level floorplanning and analog block integration is desirable
- Ability to use productivity-enhancing tools and design scripts to further automate tasks is also desirable

EDUCATION & EXPERIENCE:

- 4 year degree, BSEE is preferred (or related field) or equivalent education + 8 years experience
- Minimum of 5 years of mask design experience in sub-micron CMOS (16nm or below is a plus)
- 3+ years of experience in high precision analog/mixed signal circuit, preferably in high-speed (more than 10GHz) application

ATTRIBUTES/APTITUDES/ATTITUDE

- Thrives in a highly collaborative and dynamic work environment
- Possesses a quality-oriented mindset and attention to details
- Demonstrate superb communication skills
- Strong inner drive and self-motivation
- Creativity in problem-solving