

Employee Job Description



GENERAL INFORMATION:

| | |
|-------------------|---|
| Job Title | Digital DSP Designer |
| Department | Product Development |
| Location | San Diego, CA |
| Job Level | Staff ~ Principle |
| Manager | Director, Design Engineering Technical |

JOB PURPOSE:

This position is for VIPER PHY transceiver digital DSP blocks design, and verification.

ESSENTIAL FUNCTIONS:

- Design, verify, and VLSI implementation of various high speed digital PHY communication blocks.
- Work closely with the PHY system team to co-verify DSP blocks.

QUALIFICATIONS/SKILLS:

- Experience with expertise in the design and high speed implementation of communication data path DSP blocks, such as digital adaptive equalizer, digital timing recovery unit, AGC, and coded modulation.
- Proficient with modern system design tools and languages (e.g. C++/C, SystemVerilog, Perl)
- Expertise in co-verifying complex DSP blocks.
- In depth knowledge of datapath synthesis, especially in advanced technology nodes(e.g. 40nm,28nm CMOS)
- Knowledge of high speed arithmetic implementation.
- SV/UVM based verification skill a plus
- Strong analytical, debugging and problem solving skills.

EDUCATION/EXPERIENCE

- MS/PhD in EE
- 5+ working experience in the field of digital DSP ASIC design

ATTRIBUTES/APTITUDES/ATTITUDE

- Thrives in a highly collaborative and dynamic work environment
- Possesses a quality-oriented mindset and attention to details
- Demonstrate superb communication skills
- Strong inner drive and self-motivation
- Creativity in problem-solving