

Job Description

Job Title	High Speed Analog Designer
Location	San Diego, CA
Manager	Director, Technical Design Engineering

JOB PURPOSE:

This position is for transceiver high speed SerDes design, and verification.

ESSENTIAL FUNCTIONS:

- Design low voltage low power high speed SerDes
- Work closely with physical design team on custom layout

QUALIFICATIONS/SKILLS:

- Experience with expertise in the design of high speed low voltage low power SerDes blocks up to 30Gbps, such as CTLE, VGA, driver, PLLs designs.
- Proficient with mixed-signal designs tools and languages (e.g. Cadence ADE XL, Layout XL, AMS)
- Expertise in advanced technology nodes (40nm,28nm CMOS) mixed-signal circuits front-end design and back-end physical design.
- In depth knowledge of mixed-signal verification setup, such as AMS.
- Strong analytical, and problem solving skills.

EDUCATION/EXPERIENCE

- MS/PhD in EE
- 8+ working experience in the field of high speed analog CMOS SerDes circuits design

ATTRIBUTES/APTITUDES/ATTITUDE

- Thrives in a highly collaborative and dynamic work environment
- Possesses a quality-oriented mindset and attention to details
- Demonstrate superb communication skills
- Strong inner drive and self-motivation
- Creativity in problem-solving