

# Employee Job Description



## GENERAL INFORMATION:

<b>Job Title</b>	<b>Place and Route Flow Development and Implementation Engineer</b>
<b>Department</b>	<b>Engineering</b>
<b>Location</b>	<b>CVR</b>

## SUMMARY:

Working with the project design team, this individual will be responsible for completing physical implementation of all digital blocks required for the current project using the developed flows

## ESSENTIAL FUNCTIONS OF THIS POSITION:

This role works to define and implement advance node repeatable place and route flows of high speed blocks, data path and chip level integration. The flows require implementation of:

- floor-planning, power planning
- complex clock networks
- timing driven place and route
- extraction, static timing analysis, timing closure
- signal integrity
- DFM, IR/EM analysis

Additionally,

- Resolving advanced node physical verification violations
- Resolving IR/EM violations
- Working with complex Low Power High Speed Designs

## QUALIFICATIONS/SKILLS:

- Timing driven place and route
- Static timing analysis, timing closure, signal integrity
- Hands on experience current place and route EDA tools
- Knowledge of advanced node design rules
- Programming and scripting using Tcl/Tk/Unix Shell (sh, bash, csh)
- Strong analytical, debugging and problem solving skills

**EDUCATION & EXPERIENCE:**

- BS in EE or CS required; MS strongly preferred
- 10+ years in flow development and implementation of digital blocks. SOC implementation is a plus
- Experience in all aspects of the place and route flow, RTL to GDSII

**ATTRIBUTES/APTITUDES/ATTITUDE**

- Thrives in a highly collaborative and dynamic work environment
- Possesses a quality-oriented mindset and attention to details
- Demonstrate superb communication skills
- Strong inner drive and self-motivation
- Creativity in problem-solving